

**DIGITAL BLANKING CIRCUIT**ABSTRACT OF THE DISCLOSURE

5 A digital blanking circuit allows a first digital input signal transition to be passed on to a following stage, but prohibits the passing of subsequent transitions for a predetermined blanking interval. One embodiment of the present invention employs rising edge and falling edge latches, the inputs of which receive the digital input signal and the outputs of which are connected to a two-to-one multiplexer. The mux output is  
10 connected to a blanking interval circuit, which is triggered to begin timing a blanking interval by a multiplexer output transition. The blanking interval circuit provides outputs which control the latches and selects the latch output to be transferred to the  
15 multiplexer output such that the multiplexer output is prevented from transitioning during a blanking interval. An "adaptive" blanking circuit is also described in which the blanking interval is terminated when the transition which triggered the start of the blanking interval propagates through an entire signal path, such that the  
20 blanking interval is automatically adjusted to be the same as the signal path delay.

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